R16

Reg. No:

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)
B.Tech III Year I Semester Regular & Supplementary Examinations Nov/Dec 2019
SOFTWARE ENGINEERING & ARCHITECTURE
(Computer Science & Engineering)

Time: 3 hours   Max. Marks: 60

(Answer all Five Units **5 x 12 = 60** Marks)

UNIT-I

1	**a** Explain PSP & TSP framework activities.	**6M**
	b Explain the term Software Engineering – A Layered Technology.	**6M**

OR

2	Explain in detail about Process Assessment and different approaches in it.	**12M**

UNIT-II

3	**a** Explain in detail about Communication Principles/Practices.	**6M**
	b Explain in detail about Planning Principles/Practices.	**6M**

OR

4	What is Use-case? Why it is used? How it helps in analyzing the requirements? Explain with an example.	**12M**

UNIT-III

5	What is activity diagram? Draw the activity diagram for ATM system.	**12M**

OR

6	**a** Write the guidelines for quality design.	**6M**
	b Write about quality attributes in detail.	**6M**

UNIT-IV

7	Write about the metrics for Process, Project and Product with an example and the Functional Point (FP) for a software product.	**12M**

OR

8	**a** What is the difference between Verification and Validation? Distinguish it.	**6M**
	b What is Art of debugging? Explain.	**6M**

UNIT-V

9	Explain in detail about Architectural Styles.	**12M**

OR

10	How Heterogeneous Architectures helps in solving the problems?	**12M**

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